

JUN 17 2005

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073338.0178

PATENT APPLICATION
USSN 10/698,953



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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of: Jiang, et al.
Serial No.: 10/821,034
Filing Date: April 8, 2004
Confirmation No. 9246
Group Art Unit: 2819
Title: *Determining Analog Error Using Parallel Path Sampling*

Mail Stop Issue Fee
Commissioner for Patents
P.O. Box 1450
Alexandria, Virginia 22313-1450

Dear Sir:

06/20/2005 HMARZ11 00000075 10821034

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CERTIFICATE OF MAILING
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I hereby certify that this communication is being deposited with the United States Postal Service "Express Mail Post Office to Addressee" under 37 C.F.R. § 1.10 on the date indicated below and is addressed to: Mail Stop: Amendment; Commissioner For Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Willie Jiles

Willie Jiles

Date: *June 14, 2005*

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INFORMATION DISCLOSURE STATEMENT

Applicants respectfully request, pursuant to 37 C.F.R. §§ 1.56, 1.97 and 1.98, that these references listed on the attached PTO-1449 form be considered and cited in the examination of the above-identified patent application. No representation is made that a search has been made, that these references are material to the patentability of the present invention, or that these references qualify as prior art.

The present Application was filed after June 30, 2003. Therefore, under the July 11, 2003, waiver of 37 C.F.R. § 1.98(a)(2)(i) by the U.S. Patent and Trademark Office, copies of U.S. Patents and U.S. Patent Application Publications listed in the attached PTO-1449 form are not enclosed. Copies of all other references are enclosed for the convenience of the Examiner.

Pursuant to 37 C.F.R. § 1.97(d) and § 1.97(e), Applicants hereby certify that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Information Disclosure Statement.

Pursuant to 37 C.F.R. § 1.97(d) and § 1.97(e) Applicants hereby certify that each item of information contained in this Information Disclosure Statement was first cited in a communication from a foreign patent office in a counterpart application and that this communication was not received by any individual designated in 37 C.F.R. § 1.56(c) more than thirty days prior to the filing of this Information Disclosure Statement.

REMARKS

Pursuant to 37 C.F.R. § 1.97(d), Applicants respectfully request consideration of the references listed on the attached PTO-1449 form. The fee and certification required by 37 C.F.R. § 1.97(d) are provided, thus such consideration is required even though a Notice of Allowance has been mailed. Pursuant to 37 C.F.R. § 1.17(p), the stipulated fee of \$180.00 is enclosed herewith. Although Applicants believe no other fee is due, the Commissioner is hereby authorized to charge any fee to Deposit Account No. 02-0384 of Baker Botts L.L.P.

Respectfully submitted,

BAKER BOTTS L.L.P.
Attorneys for Applicants

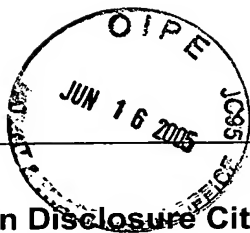


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PTO-1449 Information Disclosure Citation in an Application	Application No. 10/821,034	Applicant(s) Jiang, et al.	
	Docket Number 073338.0178	Group Art Unit 2819	Filing Date April 8, 2004

U.S. PATENT DOCUMENTS

		DOCUMENT NO.	DATE	NAME	CLASS	SUBCLASS	FILING DATE
	A	5,266,952	11/30/93	Stone, et al.	341	156	03/30/1992
	B	6,313,780	11/06/01	Hughes, et al.	341	156	09/27/1999
	C	2003/0080894 A1	05/01/03	Konno	341	162	10/25/2001
	D	2002/0175843 A1	11/28/02	Sonkusale, et al.	341	120	05/23/2002
	E						
	F						
	G						
	H						
	I						
	J						
	K						

FOREIGN PATENT DOCUMENTS

		DOCUMENT NO.	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
	L	WO 2004/010586	07/08/03	PCT	H03M	1/00	x	
	M							
	N							
	O							

NON-PATENT DOCUMENTS

		DOCUMENT (Including Author, Title, Source, and Pertinent Pages)	DATE
	P	European Search Report mailed May 25, 2005 re EP Application No. 05 25 0673 (3 pages)	05/25/2005
	Q	Bracey, et al., "A 70 MS/s 8-Bit Differential Switched-Current CMOS A/D Converter Using Parallel Interleaved Pipelines" pp. 143-146, © 1995 IEEE.	05/1995
	S		

EXAMINER	DATE CONSIDERED
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EXAMINER: Initial if citation considered, whether or not citation is in conformance with MPEP § 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to the applicant.